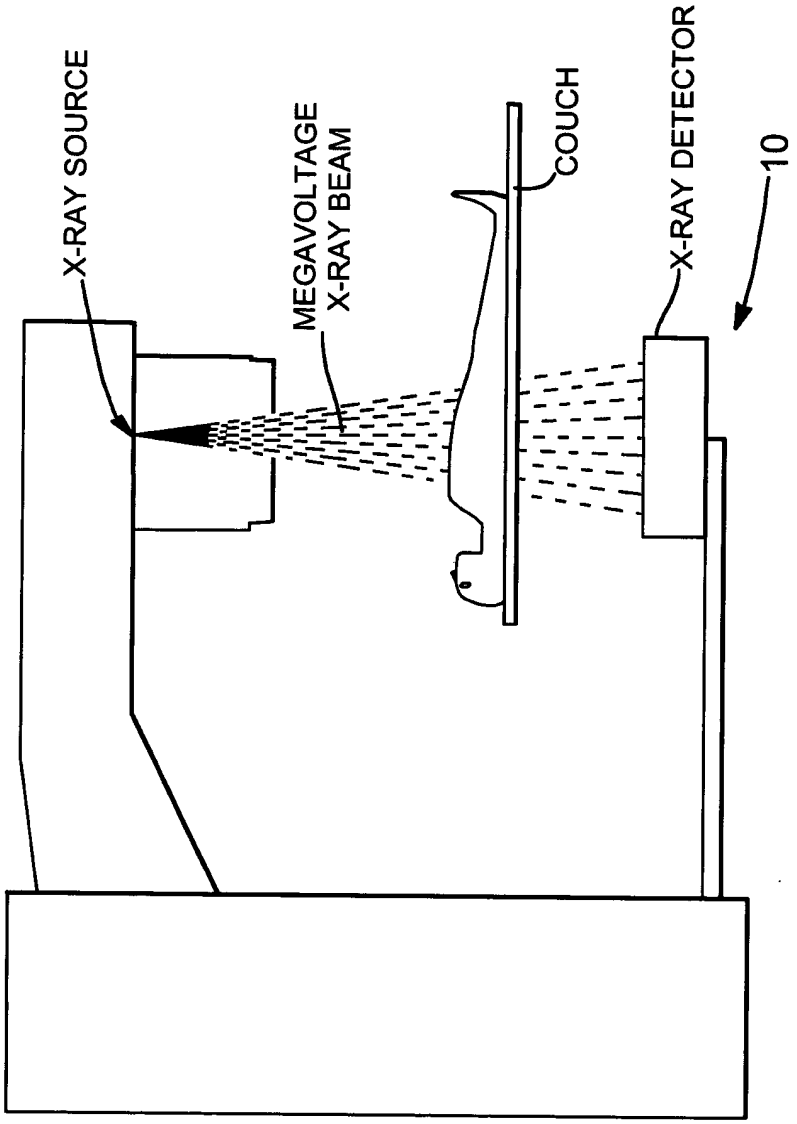


FIG. 1

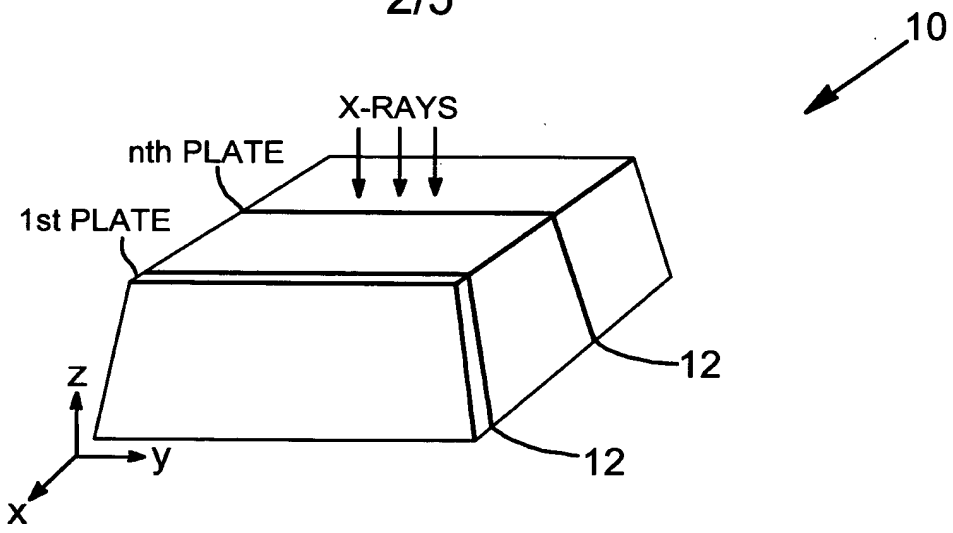
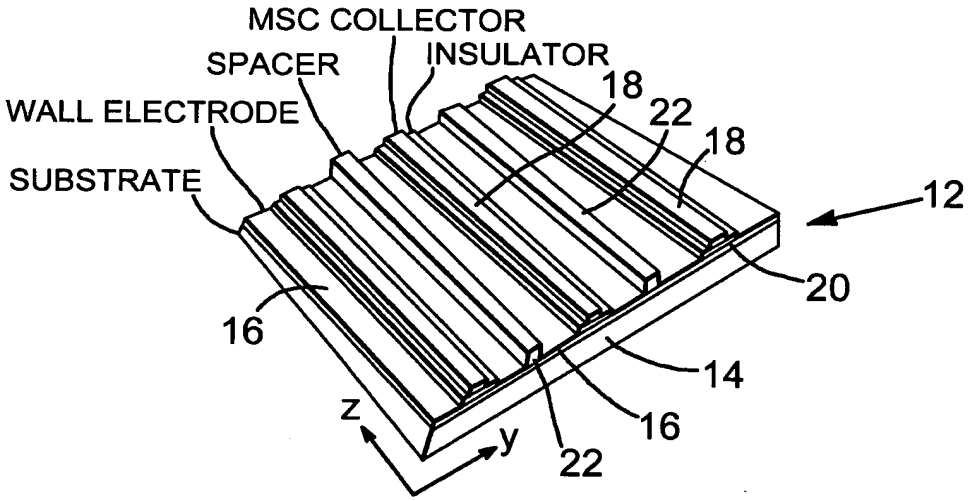
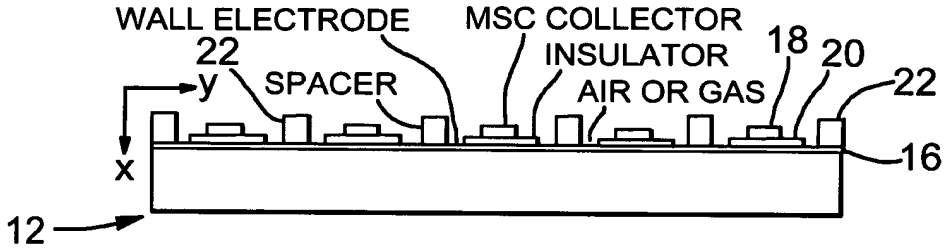


FIG. 2



AN INDIVIDUAL PLATE

FIG. 3a



CROSS SECTION OF THE PLATE IN FIG. 3a

FIG. 3b

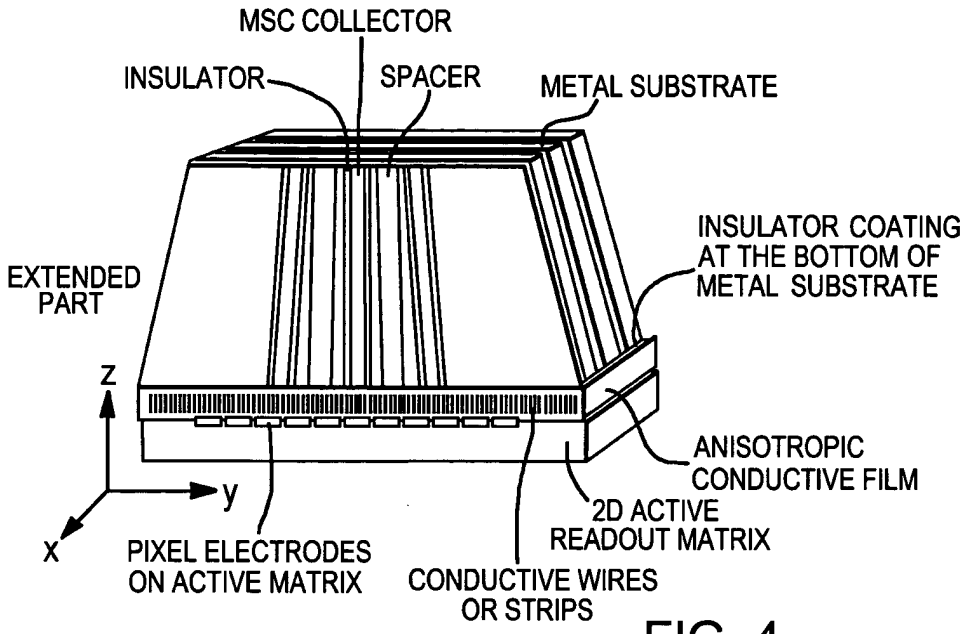

MSC COLLECTOR
INSULATOR
SPACER
METAL SUBSTRATE
INSULATOR COATING AT THE BOTTOM OF METAL SUBSTRATE
EXTENDED PART
Z
y
x
ANISOTROPIC CONDUCTIVE FILM
2D ACTIVE READOUT MATRIX
PIXEL ELECTRODES ON ACTIVE MATRIX
CONDUCTIVE WIRES OR STRIPS

FIG. 4

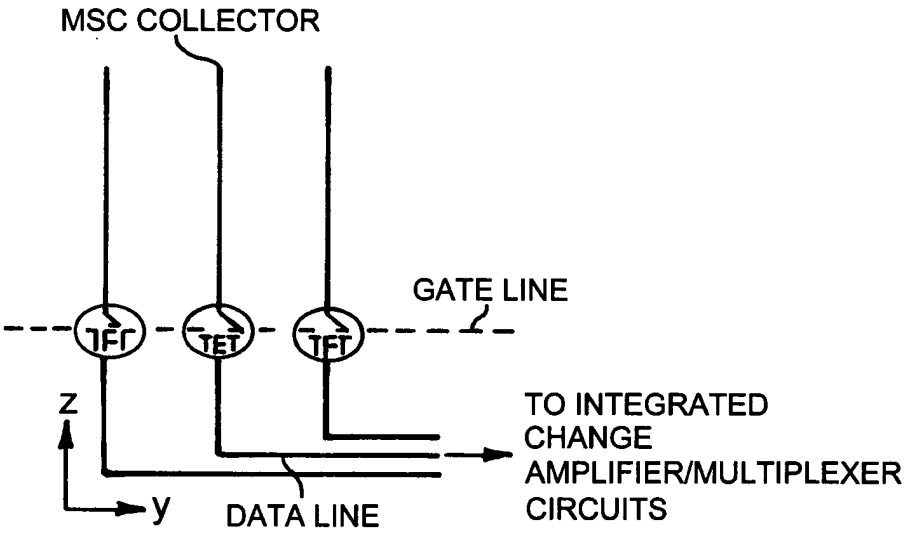

MSC COLLECTOR
GATE LINE
TFT
TFT
TFT
Z
y
DATA LINE
TO INTEGRATED CHANGE AMPLIFIER/MULTIPLEXER CIRCUITS

FIG. 5

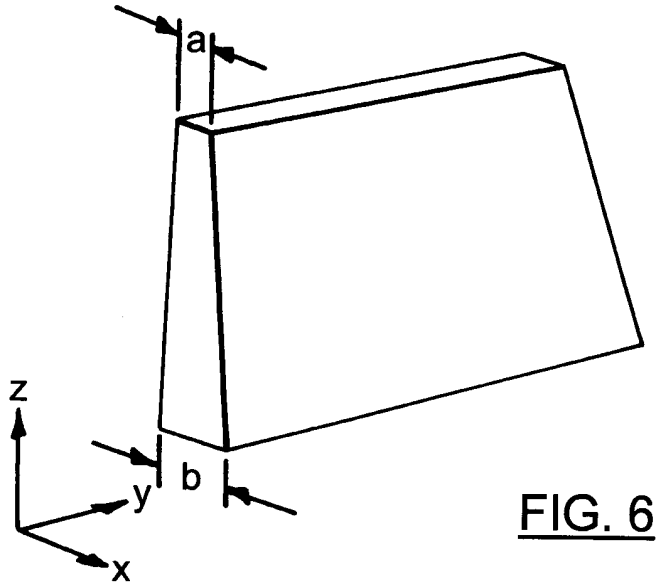


FIG. 6

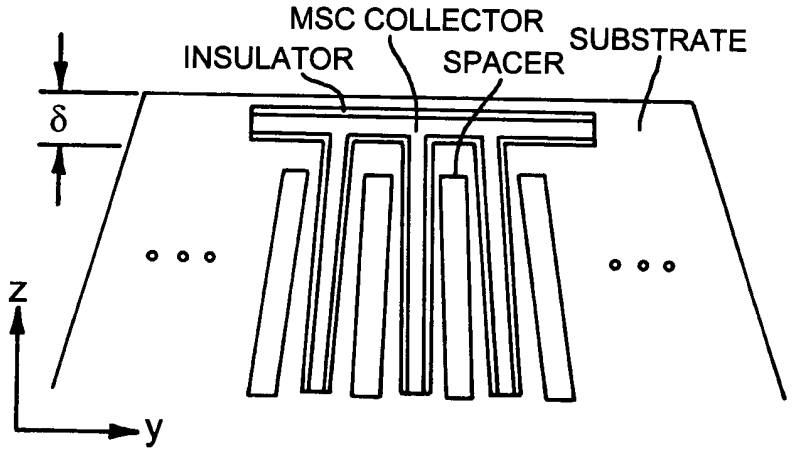


FIG. 7

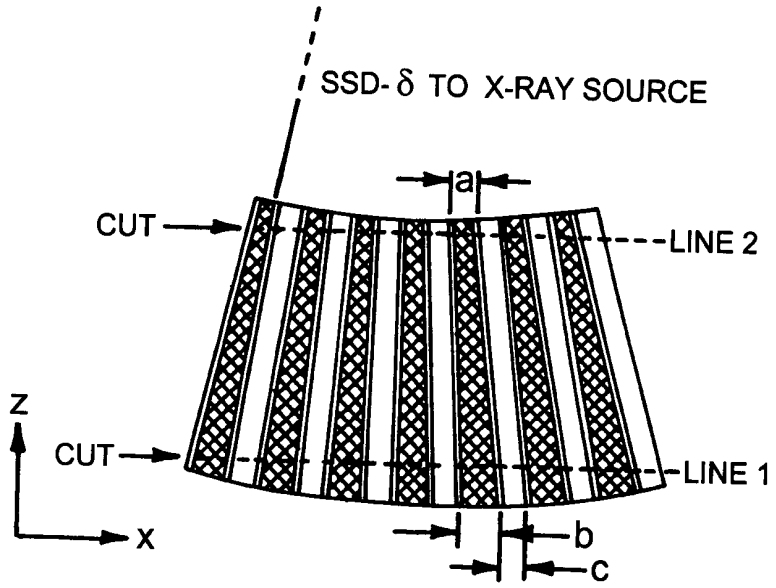


FIG. 8

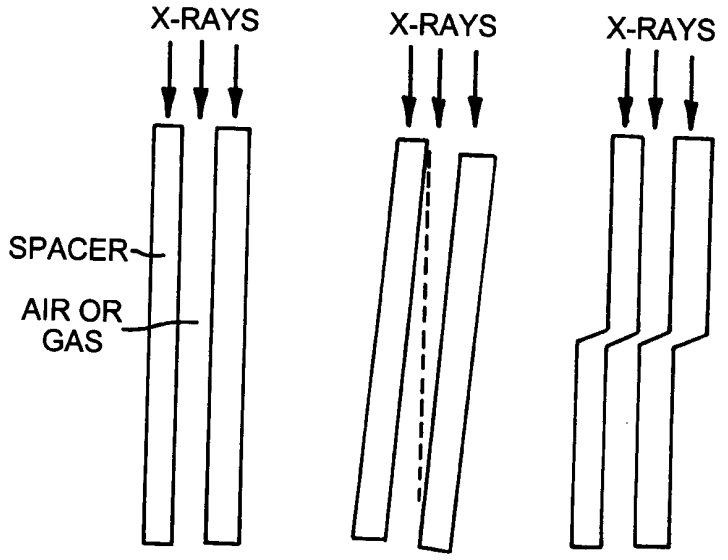


FIG. 9a FIG. 9b FIG. 9c